



**GR20  
D I I S**

**Gokaraju Rangaraju Institute of Engineering and Technology  
(Autonomous)**

**M.Tech. I Year I Semester Supplementary Examinations (GR20) Time Table September 2024**

*Timings: 10.00 AM TO 01.00 PM*

| <b>Date<br/>Branch</b> | <b>06/09/2024, FN<br/>(Friday)</b>   | <b>10/09/2024, FN<br/>(Tuesday)</b>                                    | <b>12/09/2024, FN<br/>(Thursday)</b>                         | <b>18/09/2024, FN<br/>(Wednesday)</b>   | <b>20/09/2024, FN<br/>(Friday)</b>   | <b>21/09/2024, FN<br/>(Saturday)</b>   |
|------------------------|--|--|--|---|--|--|
| <b>STRU<br/>(20)</b>   | <b>GR20D5001</b><br>Matrix Methods in<br>Structural Analysis                           | <b>GR20D5002</b><br>Advanced Solid<br>Mechanics                        | <b>GR20D5004</b><br>Advanced Concrete<br>Technology          | <b>GR20D5006</b><br>Analytical and<br>Numerical Methods for<br>Structural Engineering | <b>GR20D5011</b><br>Research Methodology and IPR<br>(Common to All Programs) | <b>GR20D5152</b><br>English for Research Paper<br>Writing<br>(Common to All Programs)    |
| <b>PE<br/>(43)</b>     | <b>GR20D5027</b><br>Power Quality  | <b>GR20D5025</b><br>Modeling and Analysis<br>of Electrical Machines    | <b>GR20D5026</b><br>Power Electronic<br>Converters           | <b>GR20D5032</b><br>Electric and Hybrid<br>Vehicles                                   | <b>GR20D5011</b><br>Research Methodology and IPR<br>(Common to All Programs) | <b>GR20D5152</b><br>English for Research Paper<br>Writing<br>(Common to All Programs)    |
| <b>DFM<br/>(52)</b>    | <b>GR20D5048</b><br>Advanced Computer<br>Aided Design                                  | <b>GR20D5053</b><br>Finite Element<br>Applications in<br>Manufacturing | <b>GR20D5050</b><br>Materials Technology                     | <b>GR20D5049</b><br>Advanced<br>Manufacturing<br>Processes                            | <b>GR20D5011</b><br>Research Methodology and IPR<br>(Common to All Programs) | <b>GR20D5152</b><br>English for Research Paper<br>Writing<br>(Common to All Programs)    |
| <b>VLSI<br/>(57)</b>   | <b>GR20D5072</b><br>Digital CMOS IC<br>Design  | <b>GR20D5078</b><br>VLSI Technology and<br>Design                      | <b>GR20D5074</b><br>Nano Fabrication and<br>Wafer Technology | <b>GR20D5071</b><br>Digital System Design<br>using HDL                                | <b>GR20D5011</b><br>Research Methodology and IPR<br>(Common to All Programs) | <b>GR20D5152</b><br>English for Research Paper<br>Writing<br>(Common to All<br>Programs) |
| <b>CSE<br/>(58)</b>    | <b>GR20D5094</b><br>Mathematical<br>Foundation for<br>Computer Science<br>Applications | <b>GR20D5095</b><br>Advanced Data<br>Structures                        | <b>GR20D5097</b><br>Data Mining                              | <b>GR20D5099</b><br>Computer System<br>Design   | <b>GR20D5011</b><br>Research Methodology and IPR<br>(Common to All Programs) | <b>GR20D5152</b><br>English for Research Paper<br>Writing<br>(Common to All Programs)    |
| <b>DS<br/>(B0)</b>     | <b>GR20D5116</b><br>Statistical Methods for<br>Data Science                            | <b>GR20D5118</b><br>Advanced Data<br>Structures and<br>Algorithms      | <b>GR20D5117</b><br>Data Science                             | <b>GR20D5121</b><br>Artificial Intelligence   | <b>GR20D5011</b><br>Research Methodology and IPR<br>(Common to All Programs) | <b>GR20D5152</b><br>English for Research Paper<br>Writing<br>(Common to All Programs)    |

**Note:** Any discrepancy may please be informed to COE immediately.

Date: 16-08-2024

**Controller of Examinations**